



*ENS015 / Xtendwave selects EnSilica's eSi-3200 processor for the receiver IC implementing NIST's next-generation WWVB atomic timekeeping signal*

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**About EnSilica**

EnSilica is an established company with many years experience providing high quality IC design services to customers undertaking FPGA and ASIC designs. EnSilica has an impressive record of success working across many market segments with particular expertise in multimedia and communications applications. Customers range from start-ups to blue-chip companies. EnSilica can provide the full range of IC design services, from System Level Design, RTL coding and Verification through to either a FPGA device or the physical design for ASIC designs. EnSilica also offers a portfolio of IP, including a highly configurable 16/32 bit embedded processor called eSi-RISC, the eSi-Comms range of communications IP and eSi-Crypto encryption IP. For further information about EnSilica, visit <http://www.ensilica.com>.

**About Xtendwave**

Xtendwave, based in Dallas, Texas, is an early-stage fabless semiconductor company focused on the development of physical-layer communication technologies. These are based on both analog and digital signal processing methods, strategically targeted at enabling improved performance over various media through increased data rates, greater distances, and higher quality-of-service. Xtendwave is currently engaged with customers in both the precise timekeeping industries and telecommunications industries. Xtendwave has strong collaborative relationships with three leading universities: The University of Texas at Dallas, Southern Methodist University, and The Ohio State University, and includes university faculty members on its advisory board. For further information about Xtendwave, visit <http://www.xtendwave.com>.

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